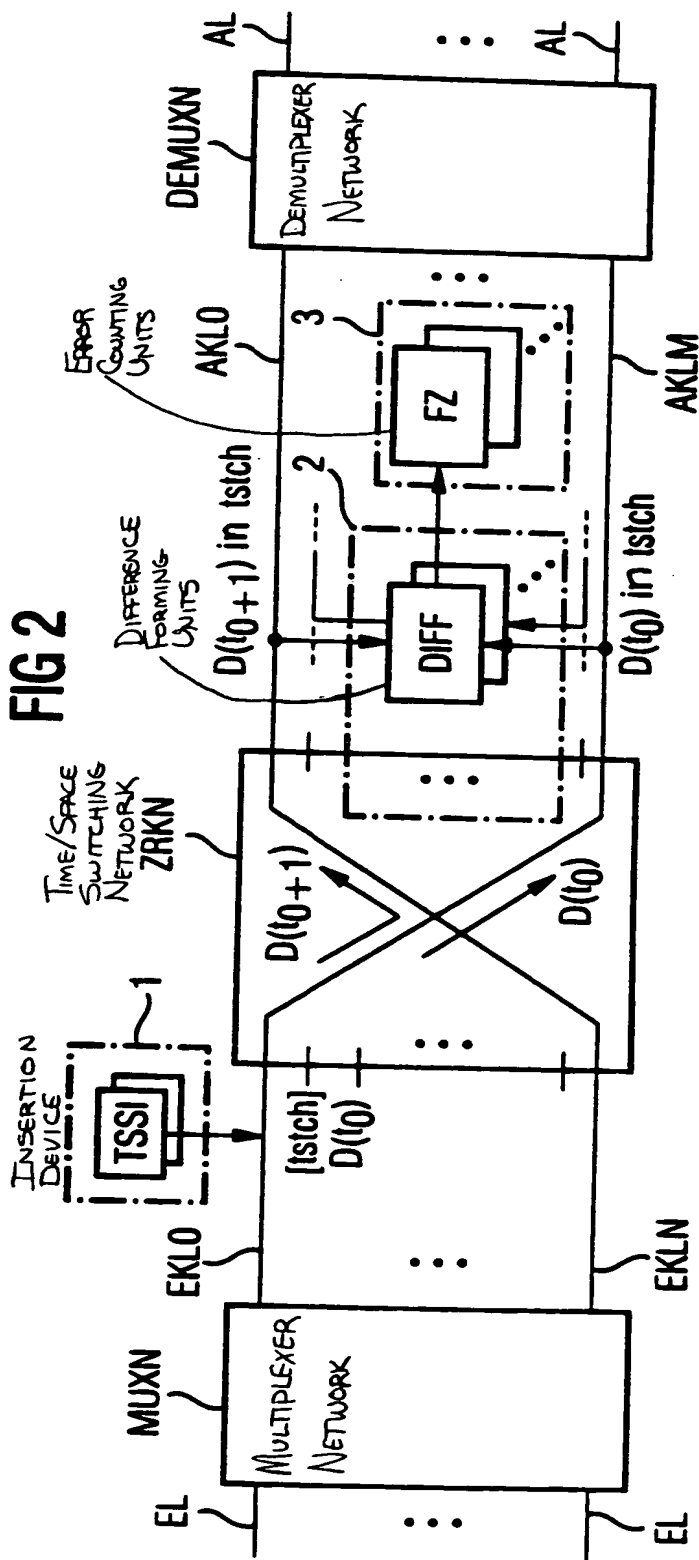


The diagram illustrates the TSSI (Time-Space Interference) concept. It shows a Time/Space Switching Network (ZRNK) with input lines EKLO, ..., AKLM, ..., EKLN and output lines F0, F1, ..., FX+2, FX+1, ..., FX. The network is a crossbar switch. Below the network, a timeline shows the sequence of events: B(F1), A(F1), A(F0), B(F0), A(F0). The events are grouped into frames F1 and F0. The diagram illustrates how the network can cause TSSI (Time-Space Interference) when the output of one frame is delayed, leading to a conflict with the output of another frame.



$$\text{With } D(t_0) = 0 \quad (\text{FRAME } F_0)$$

$$D(t_0+1) = 0+X \quad (\text{FRAME } F_1)$$

$$D(t_0+2) = 0+2X \quad (\text{FRAME } F_2)$$

RELATIVE TEST CHANNEL-ADDRESSES IN THE RESPECTIVE BLOCK

[illegible]

FIG 4

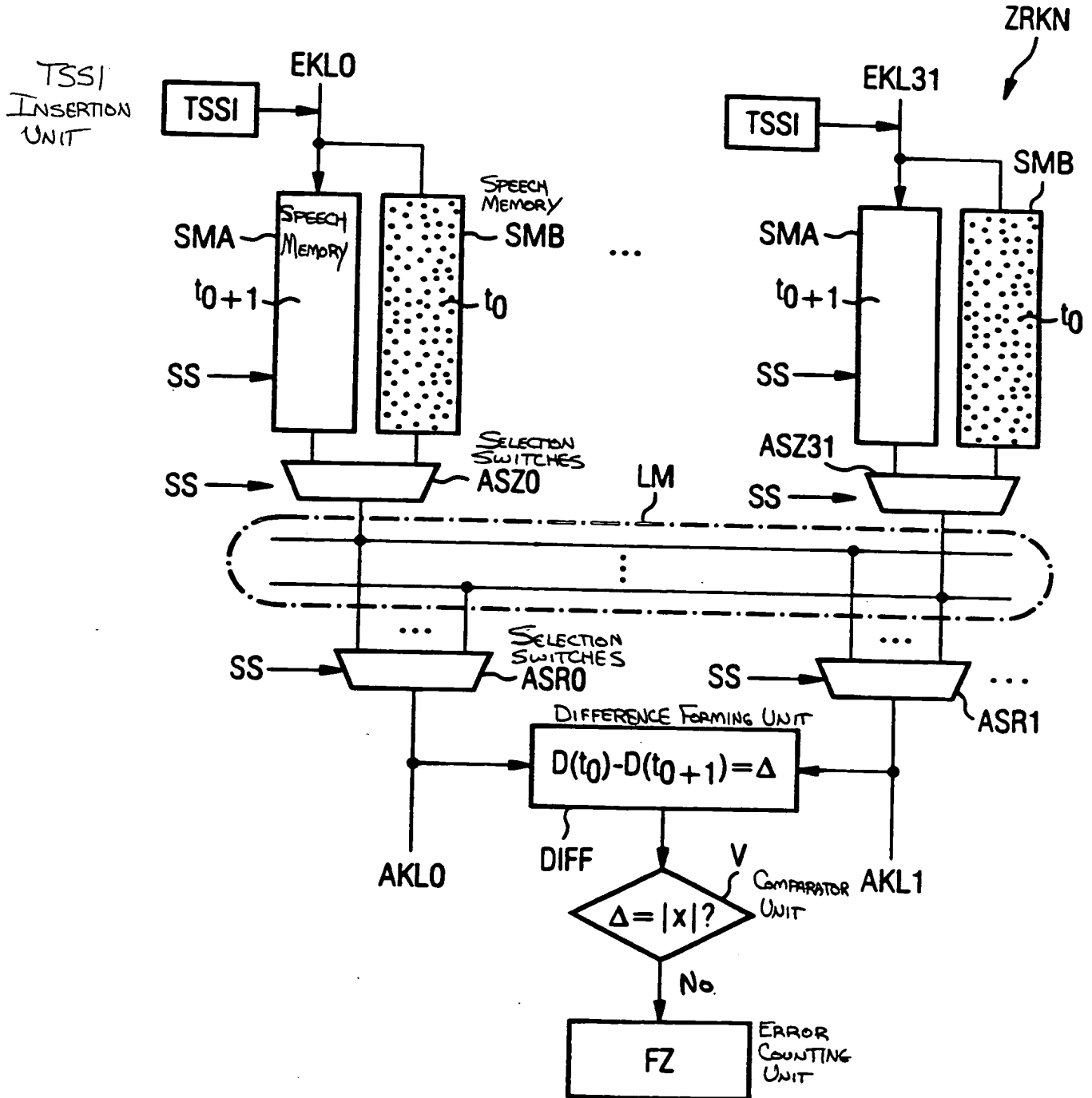


FIG 5

